



CERTIFICATE OF MAILING

I hereby certify that the correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450  
Date: March 1, 2005

Alice Sutineni

*Mic Sutineni*

PATENT  
681331-85

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: ALAIN CHAPUIS

Art Unit: 2816

Serial No.: 10/724,509

Examiner:

Filed: November 26, 2003

Title: ADAPTIVE DELAY CONTROL  
CIRCUIT FOR SWITCHED MODE POWER  
SUPPLY

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.56, Applicant submits herewith the attached Information Disclosure Statement Form PTO-1449 in connection with the above-identified patent application along with a copy of each reference (30). The statement is not a representation that all of the information cited is necessarily effective as prior art against the application.

Respectfully submitted,

Brian M. Berliner  
Attorney for Applicant  
Registration No. 34,549

Date: March 1, 2005

O'MELVENY & MYERS LLP  
400 So. Hope Street  
Los Angeles, CA 90071-2899  
Telephone: (213) 430-6000

FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. 681331-85	SERIAL NO. 10/724,509
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT  (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT: Alain CHAPUIS	
		FILING DATE: November 26, 2003 GROUP: 2816

Reference Designation      U.S. Patents

Examiner Initial	Document Number	Date	Name	Class	Filing Date Subclass if appropriate
	AA 0027101	02/12/04	Vinciarelli		
	AB 0033152	10/25/01	Pohlman et al.		
	AC 0073347	06/13/02	Zafarana et al.		
	AD 0122429	07/03/03	Zhang et al.		
	AE 0142513	07/31/03	Vinciarelli		
	AF 0201761	10/30/03	Harris		
	AG 3,660,672	05/02/72	Berger et al.		
	AH 4,194,147	03/18/80	Payne et al.		
	AI 4,538,073	08/27/85	Freige et al.		
	AJ 4,538,101	08/27/85	Shimpo et al.		
	AK 4,622,627	11/11/86	Rodriguez et al.		
	AL 5,053,920	10/01/91	Staffiere et al.		
	AM 5,073,848	12/17/91	Steigerwald et al.		
	AN 5,377,090	12/27/94	Steigerwald		
	AO 5,481,140	01/02/96	Maruyama et al.		
	AP 5,990,669	11/23/99	Brown		
	AQ 6,191,566	02/20/01	Petricek et al.		
	AR 6,211,579	04/03/01	Blair		
	AS 6,262,900	07/17/01	Suntio		
	AT 6,421,259	07/16/02	Brooks et al.		
	AU 6,429,630	08/06/02	Pohlman et al.		

OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AV "33702 Microprocessor Power Supply (3.0A)"	Analog Products MC33702 Fact Sheet; Motorola/digital dna/ Power Management Switching; pgs 1-4
AW "Motorola Switch Mode Power Supply With Multiple Linear Regulators And High Speed CAN Transceiver"	Motorola, Inc. 2002; digital dna; Analog Marketing; Rev. 2.5, 11/2002; 33394; Multi-Output Power Supply Semiconductor Technical Data
AX "Power Management Solutions For Networking Applications"; Presented by Luc Darmon	Smart Networks Developer Forum 2003 – June 4-6 Euro-Disney Paris, France; Motorola digital dna; <a href="http://www.motorola.com/sndf">www.motorola.com/sndf</a> ; pgs. 1-26
AY "Preliminary Information 1.5 A Switch-Mode Power Supply With Linear Regulator"	33701; Power Supply Integrated Circuit; Motorola Semiconductor Technical Data; Analog Marketing MC33701/D Rev. 1.0, 05/2003; Motorola digital dna; pgs. 1-24

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. 681331-85	SERIAL NO. 10/724,509
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT  (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT: Alain CHAPUIS	
	FILING DATE: November 26, 2003	GROUP: 2816

1 Reference Designation U.S. Patents

OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AZ "The 1 2-C Bus Specification" Version 2.1; January 2000; document order number: 9398 393 40011; Phillips Semiconductors; pgs. 1-46

BA "System Management Bus Specification" Smart Battery System Specifications; Revision 1.1 December 11, 1998; SBS Implementers Forum; Version 1.1; pgs. 1-39

BB "KEKB Power Supply Interface Controller Module" by A. Akiyama, T. Nakamura, M. Yoshida, T. Kubo, N. Yamamoto and T. Katoh KEK, High Energy Accelerator Research Organization, 1-1 Ohio, Tsukuba 305, Japan

BC "Magnet Power Supply Control System KEKB Accelerators" by T.T. Nakamura, A. Akiyama, T. Katoh, Ta. Kubo, N. Yamamoto, M. Yoshida, KEK, Tsukuba, Japan International Conference On Accelerator And Large Experimental Physics Control Systems, 1999, Trieste, Italy, pgs. 406 - 408

BD "Electronics Products" by Paul Birman and Sarkis Nercessian, Kepco, Inc. Flushing NY Vol. 37, No. 10, Electronic Products, March 1995; The Engineer's Magazine of Product Technology; Power Supply Special; DSO Samples Single Shots at 10 Gsamples/s Speech Recognition On A Single Chip LCD Has Flat-Panel Benefits At CRT Cost Product Update: High-Performance OP AMPS; A Hearst Business Publication; pgs. 1, 5, 33-34

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.